

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No7,118,445 B2
Patent Issue Date October 10, 2006
Application Serial No.09/814,260
Filing Date March 21, 2001
Assignee.....Micron Technology, Inc.
Inventorship Scott E. Moore et al.
Attorney's Docket No.....MI22-1663
Title: Semiconductor Workpiece Processing Methods, A Method of Preparing
Semiconductor Workpiece Process Fluid, And a Method of Delivering Semiconductor
Workpiece Process Fluid to a Semiconductor Processor

**REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT FOR
APPLICANT MISTAKES and PTO MISTAKES (37 C.F.R. §§ 1.322(a) and 1.323)**

To: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: Decision and Certificate of Correction
Branch of the Patent Issue Division

From: James D. Shaurette (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

Sir:

It is hereby requested that a Certificate of Correction be issued with respect to Patent No. 7,118,445 B2, granted October 10, 2006, in accordance with the Certificate of Correction form attached hereto.

It is noted that errors appear in this patent of a typographical nature of character, as more fully described below. The errors occurred in good faith. Correction thereof does not involve such changes in the patent as would constitute new matter or would require re-examination.

The exact page and line number where the errors occur in the application file are:

Page 39, line 13;

Page 50, line 5.

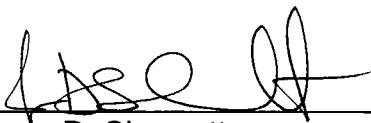
Other errors listed on the Certificate of Correction form were apparently incurred through the fault of the PTO as will be disclosed by the records of files in the Office.

Attached hereto, is Form PTO-1050, being suitable for printing.

The Director is hereby authorized to charge the \$100.00 fee as required under 37 CFR 1.20(a), or credit any overpayment, to Deposit Account No. 23-0925.

Respectfully submitted,

Dated: 3/19/08

By: 
James D. Shaurette
Reg. No. 39,833